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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Paul A. Farrar

Examiner: Dao H Nguyen

Patent No.: 7,394,157

Group Art Unit: 2818

Issue Date: July 1, 2008

Docket No: 1999-0781.02/US

Title: INTEGRATED CIRCUIT AND SEED LAYERS

Commissioner for Patents
Office of Patent Publication
ATTN: Certificate of Correction Branch

We are transmitting herewith the attached:

- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.

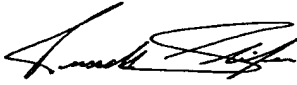
Certificate
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of Correction

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Customer Number: 26809

Respectfully submitted,

Date: July 28, 2008


By: 

Russ Slifer
Reg. No. 39,838

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Christine Hartness

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Patentee: Paul A. Farrar

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REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.


As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

Customer Number: 26809

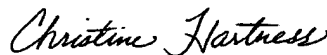
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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,394,157

Page (1) of 1

DATED : July 1, 2008

INVENTOR(S) : Farrar

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 28, line 20, in Claim 28, delete "24" and insert - - 23 - -, therefor.

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INVENTOR(S) : Farrar

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